AMENDMENTS TO THE SPECIFICATION

Please replace Paragraph [0001] with the following amended paragraph:

[0001]   This application discloses subject matter related to the subject matter disclosed in the following commonly owned co-pending patent application(s): (i) "System And Method For Synchronizing Data Transfer Across A Clock Domain Boundary," filed 6/22/01, Application [[Ser.]] No.: 09/887,793 (Docket Number 10010788-1), in the name(s) of: Richard W. Adkisson; (ii) "SYNC Pulse Compensation And Generation In A Clock Synchronizer Controller," filed 6/22/01, Application [[Ser.]] No.: 09/887,797 (Docket Number 10012773-1), in the name(s) of: Richard W. Adkisson; and (iii) "System And Method For Transferring Data From A Higher Frequency Clock Domain To A Lower Frequency Clock Domain," filed even date herewith, ~~Ser. No.: _____~~ Application No.: 09/938,206 (Docket Number 10013097-1), in the name(s) of: Rajakrishnan Radjassamy.

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